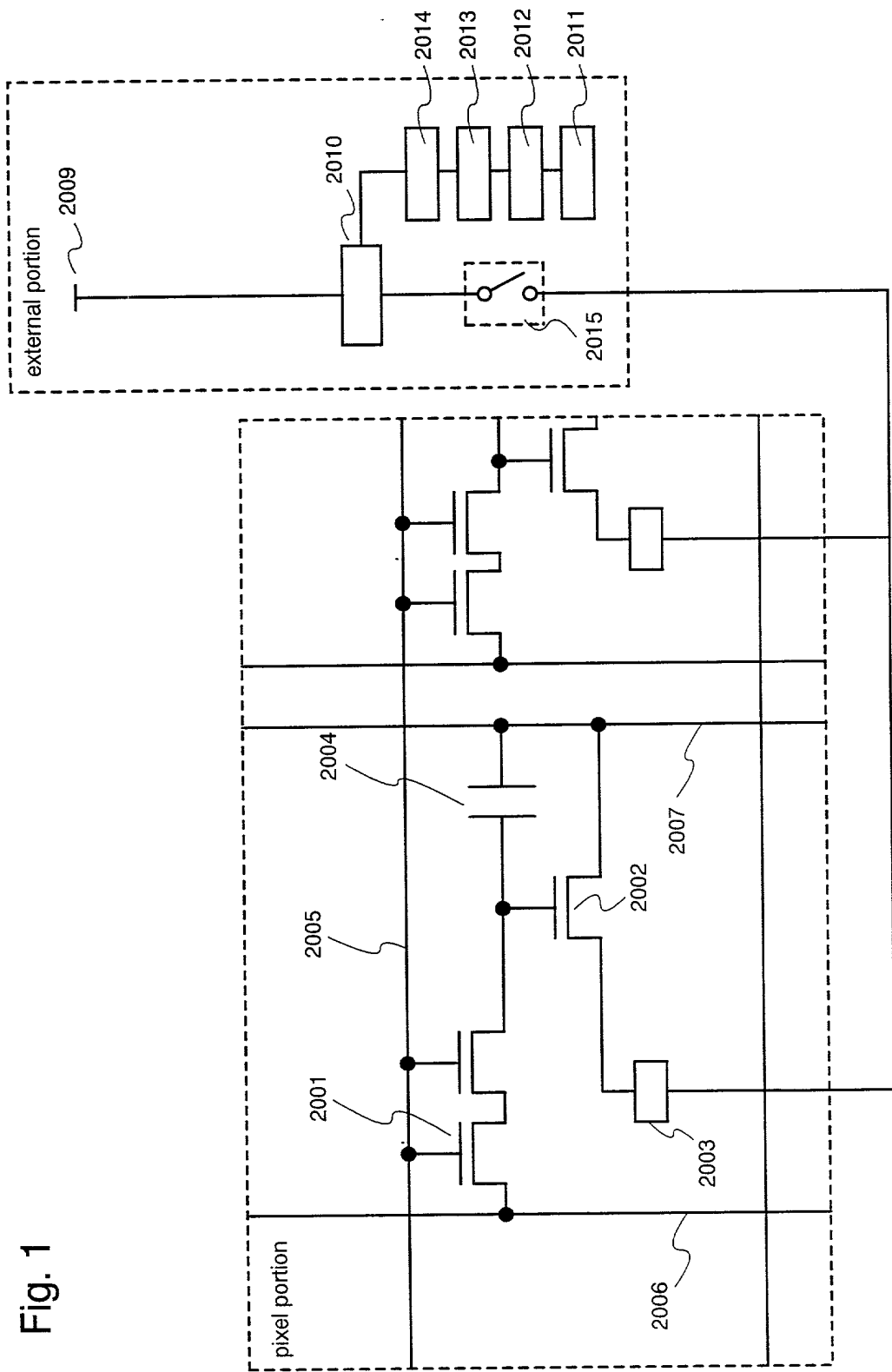


Fig. 1



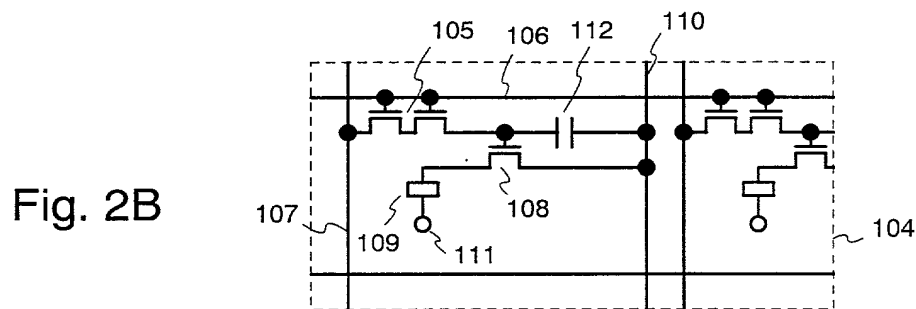
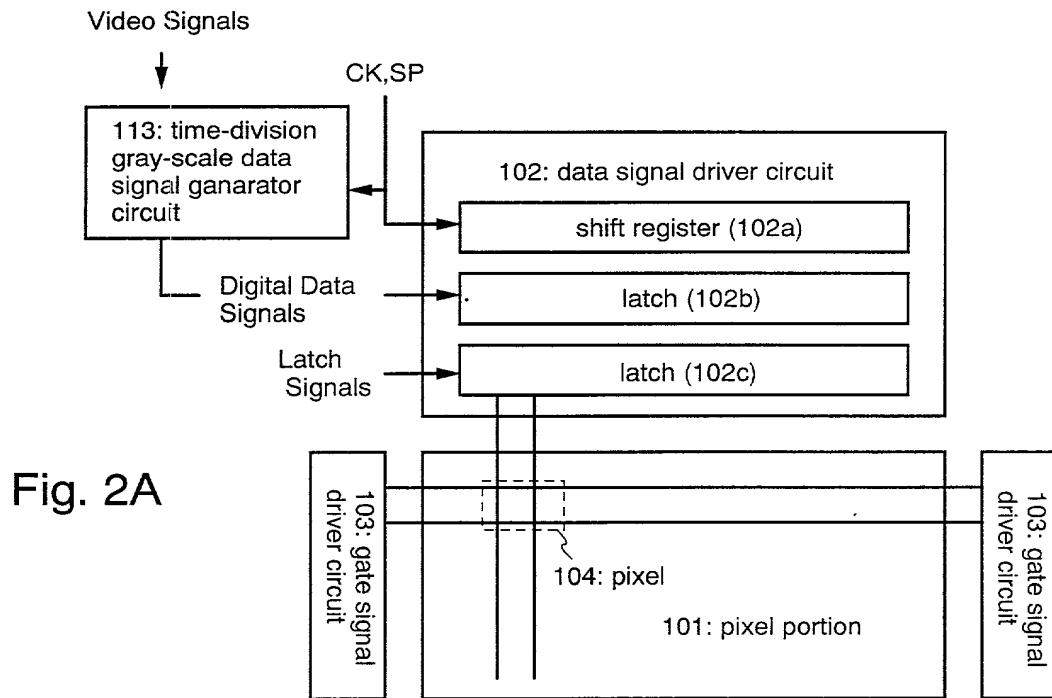


Fig. 3

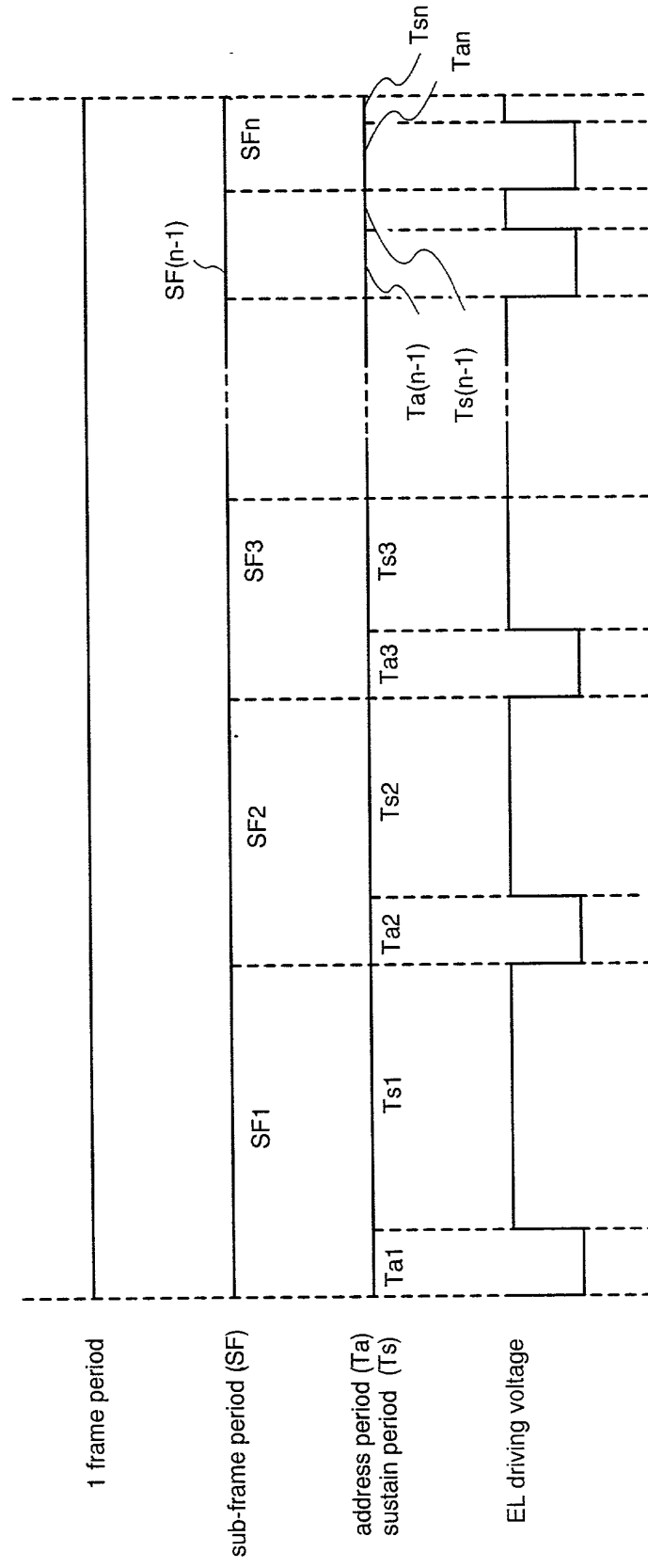


Fig. 4

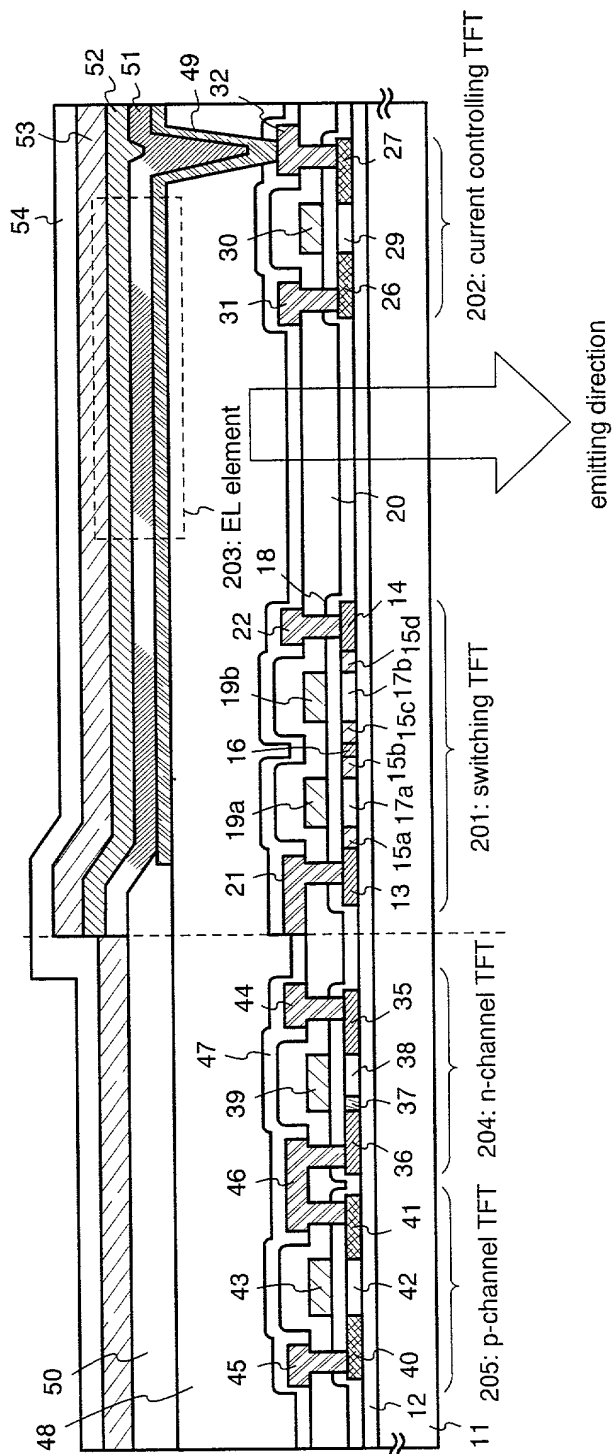


Fig. 5

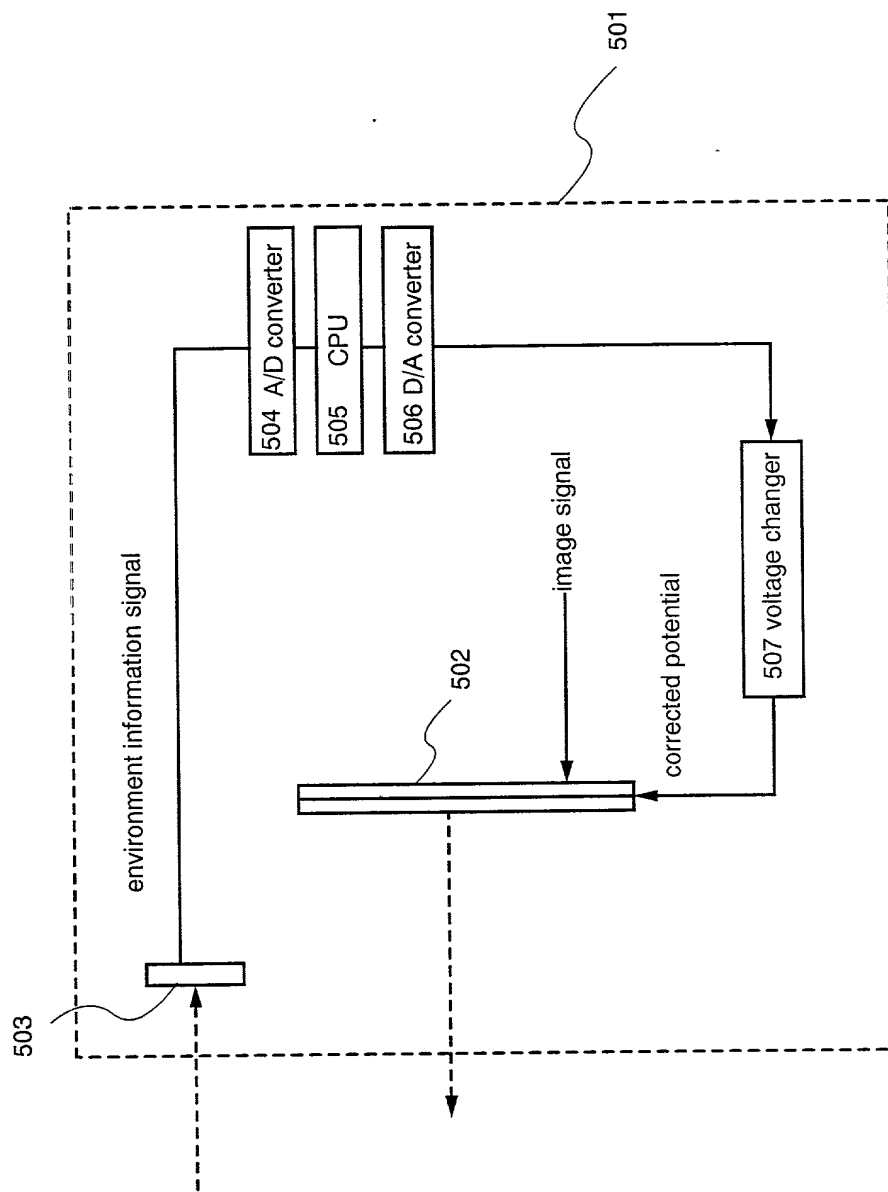


Fig. 6

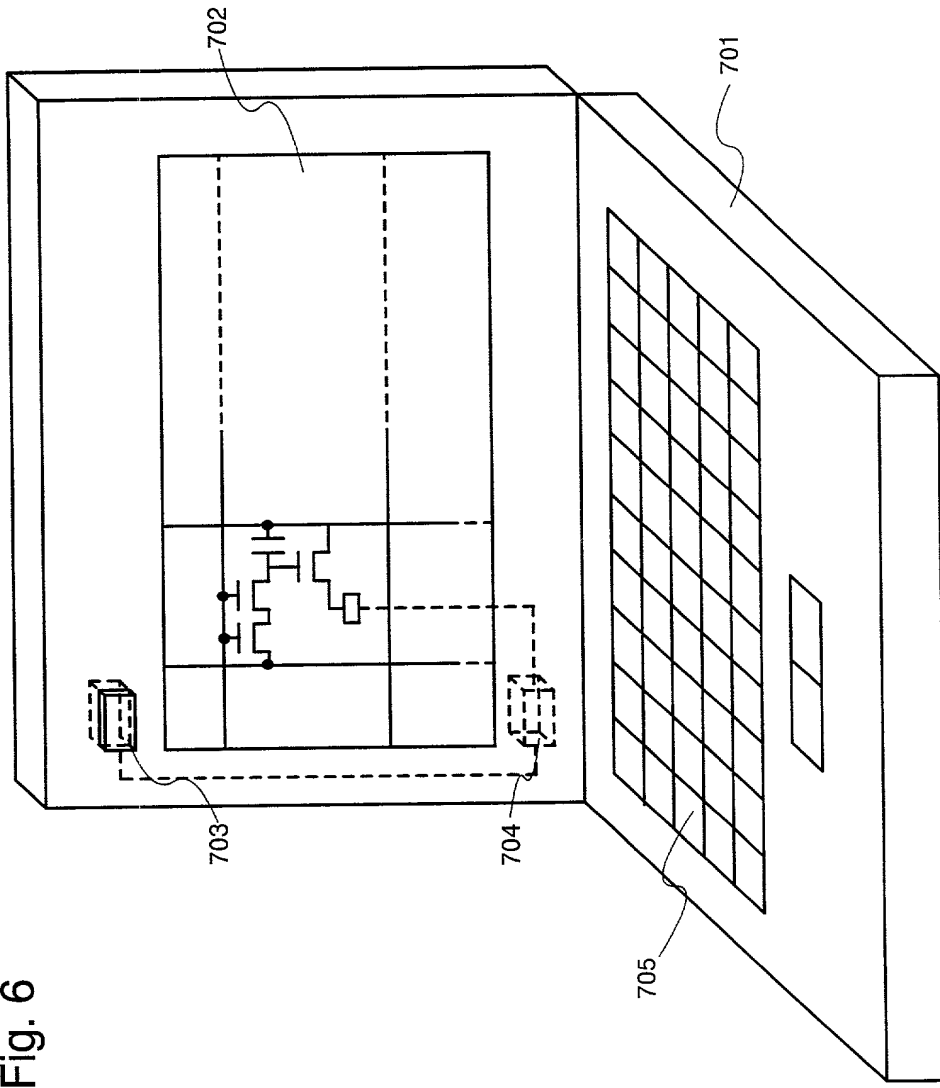


Fig. 7

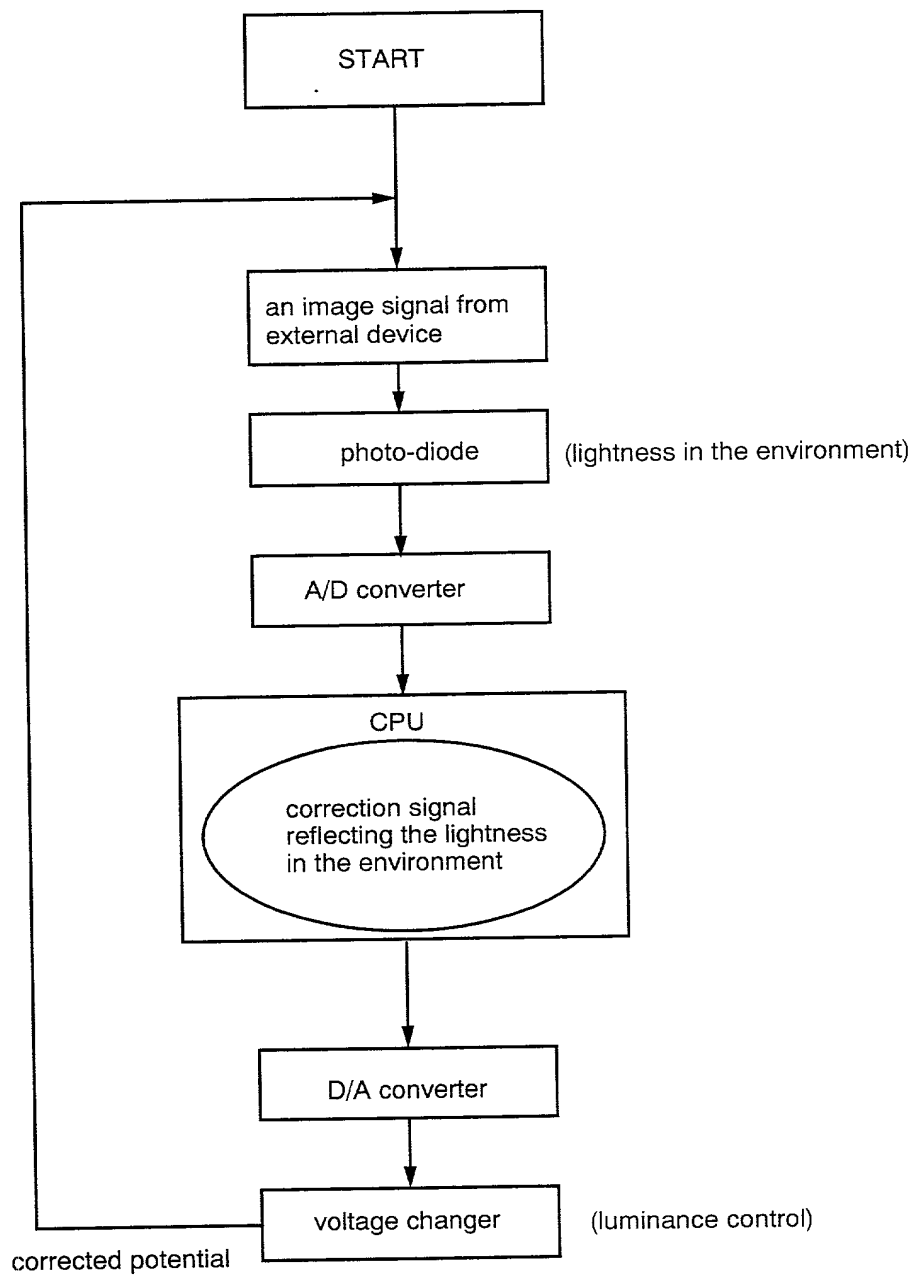
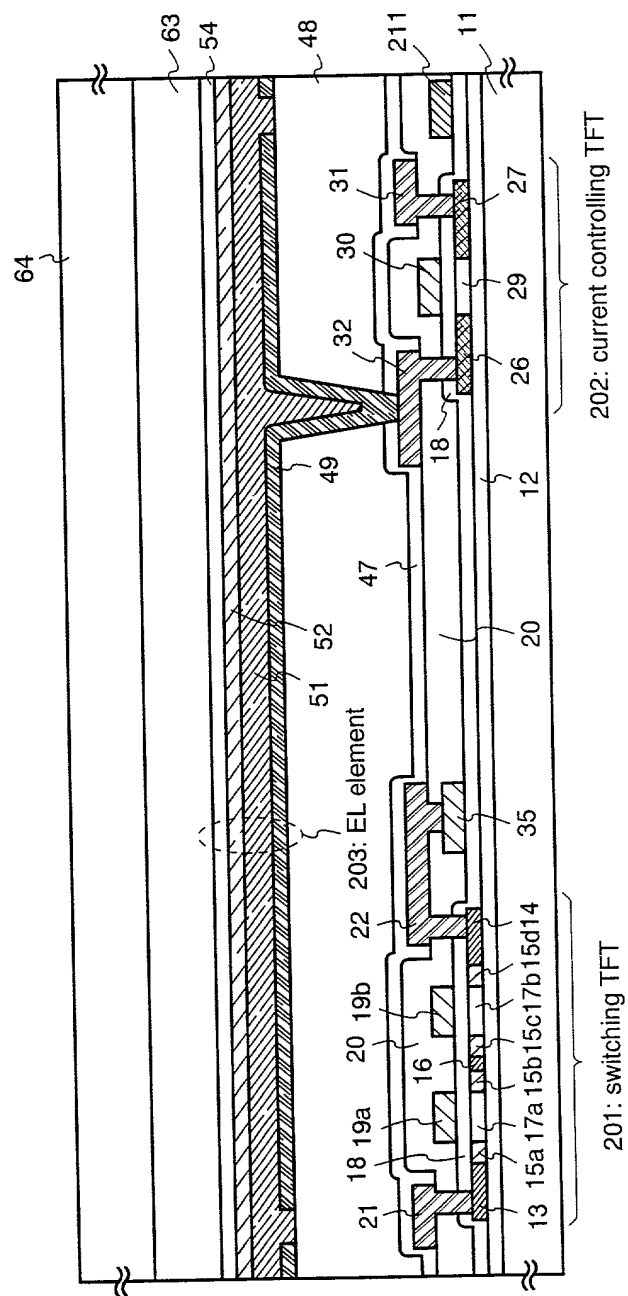


Fig. 8





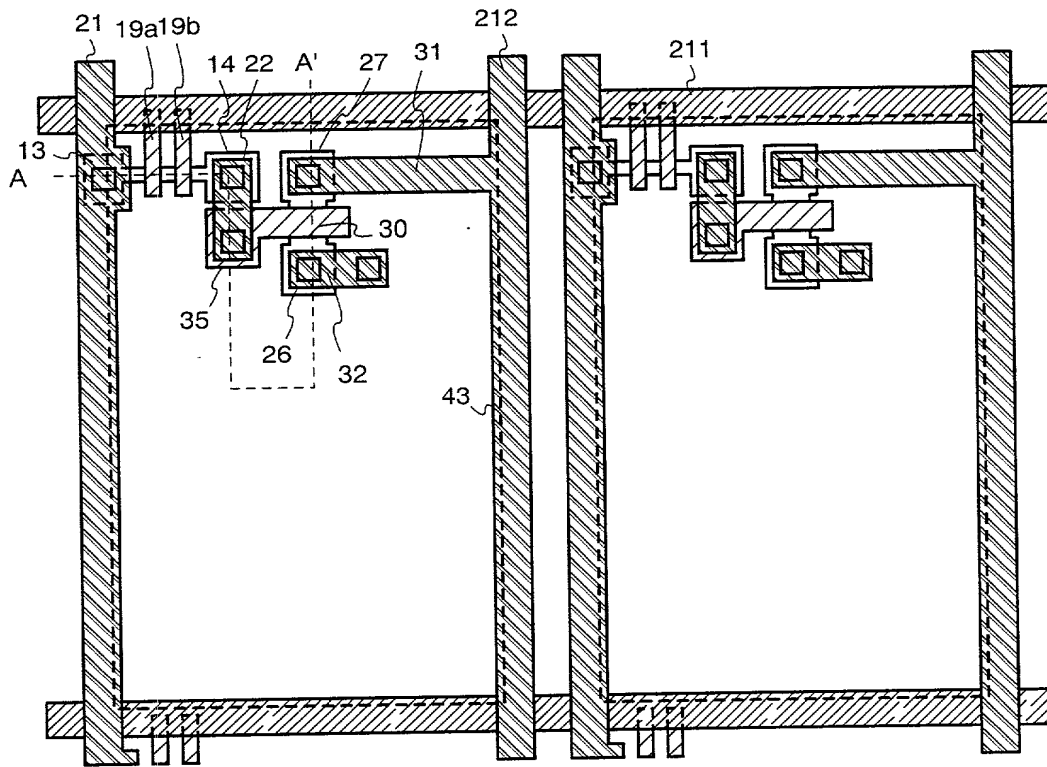


Fig. 9A

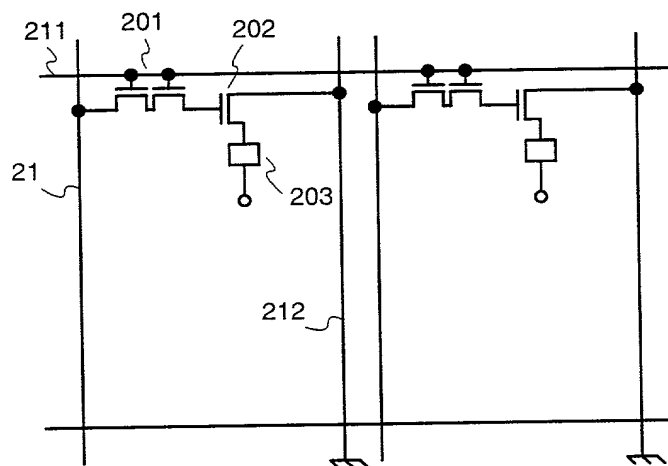


Fig. 9B

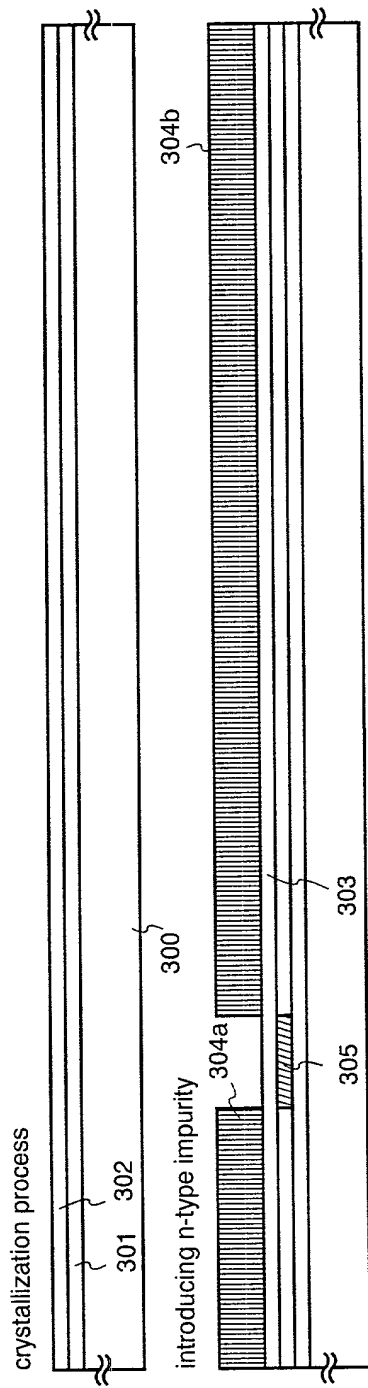


Fig. 10A

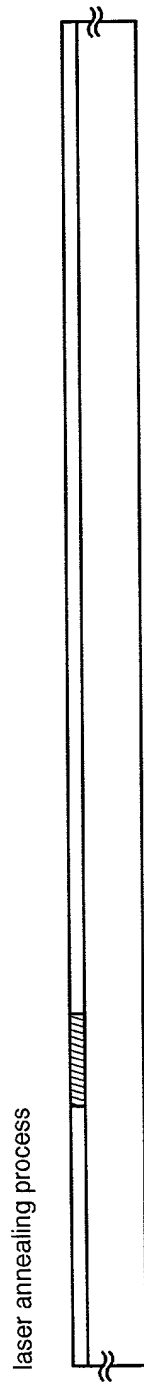


Fig. 10C

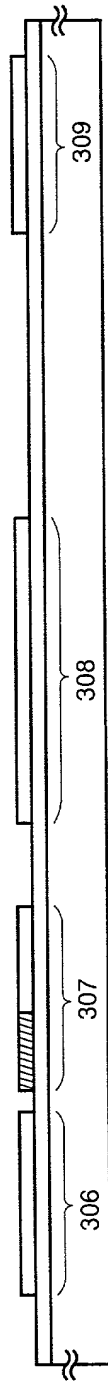


Fig. 10D

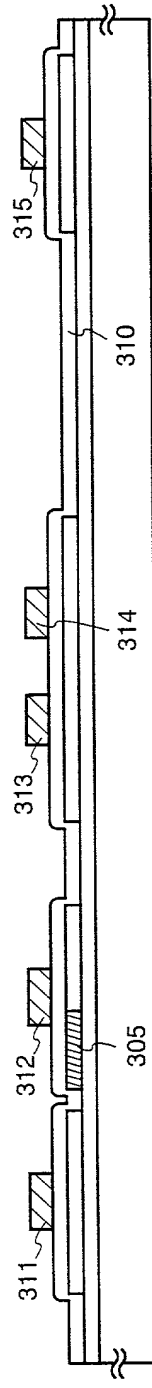
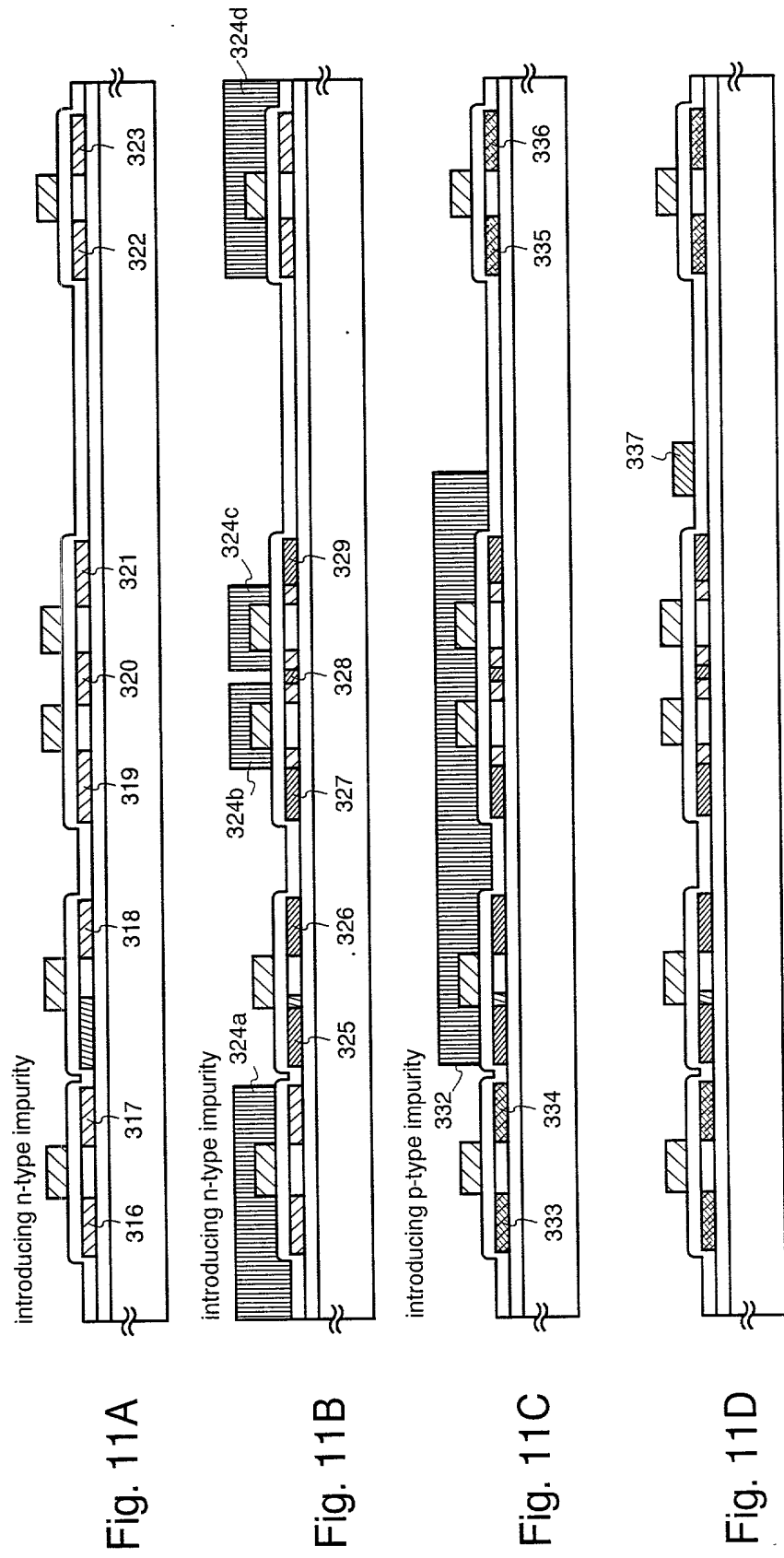


Fig. 10E



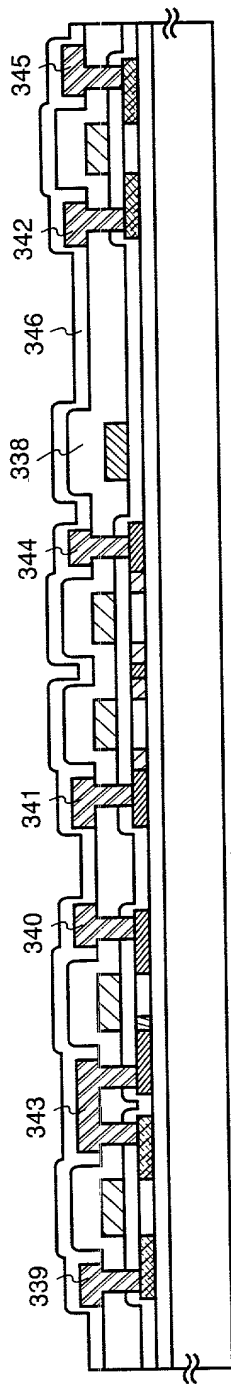


Fig. 12A

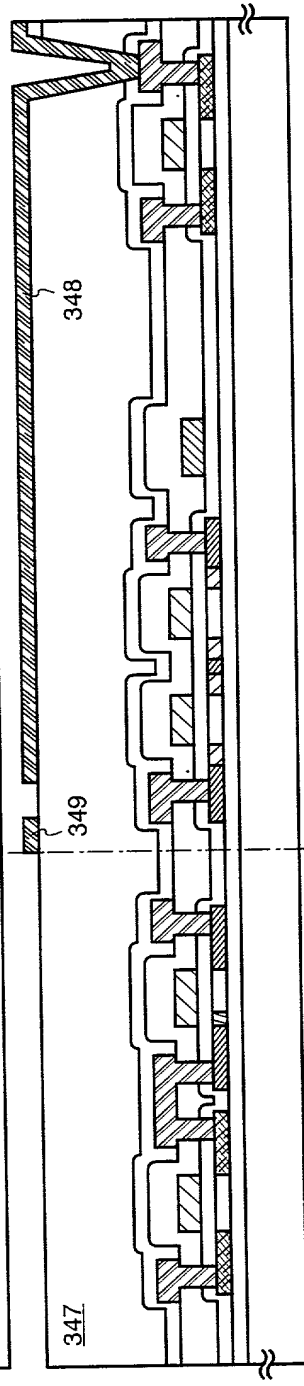


Fig. 12B

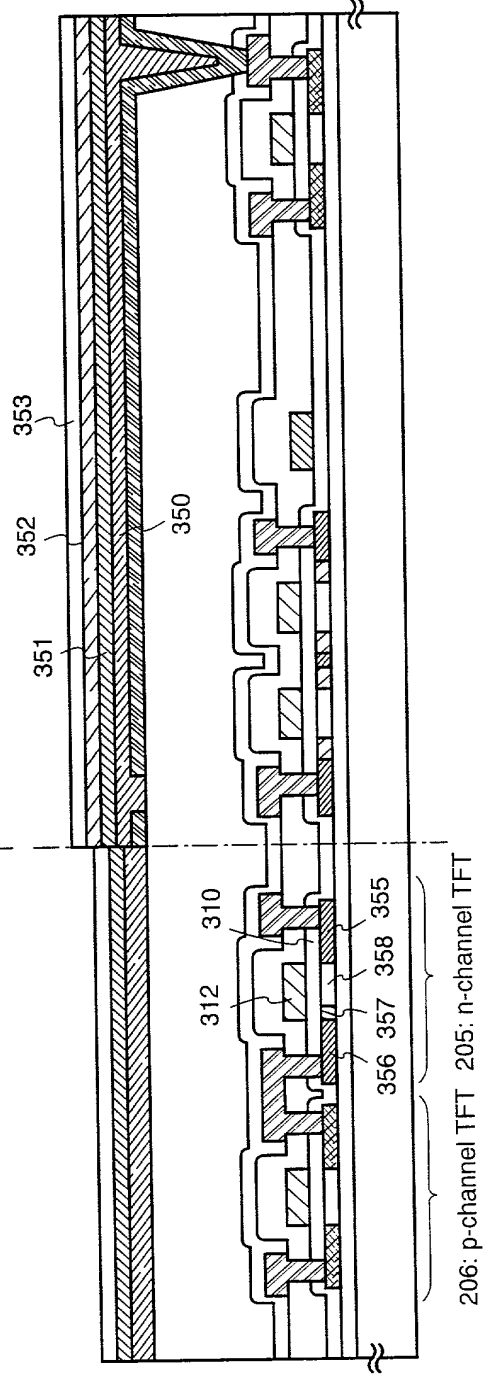


Fig. 12C

206: p-channel TFT 205: n-channel TFT

Fig. 13

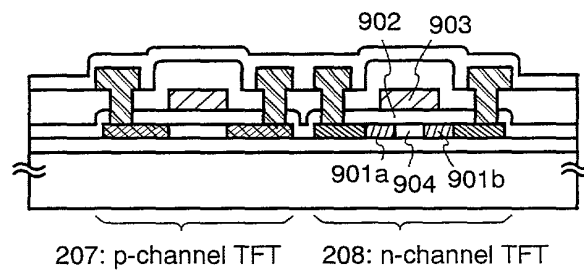
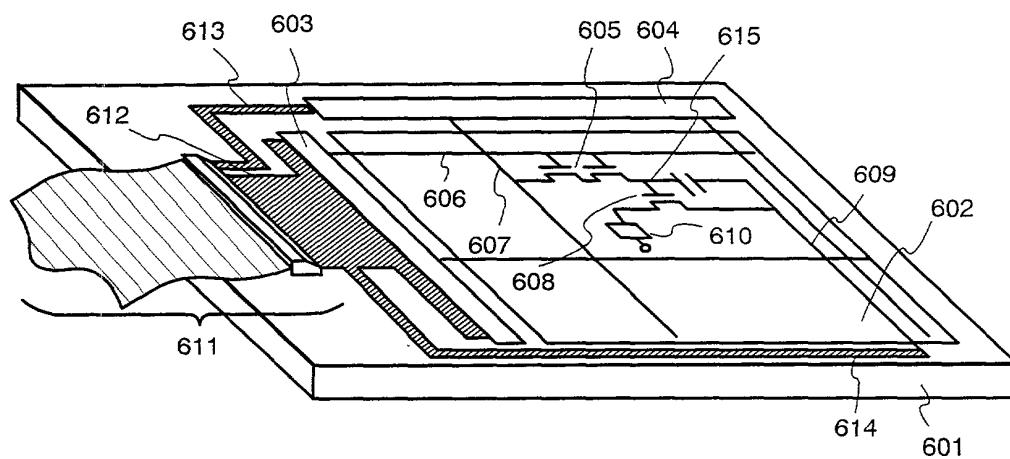


Fig. 14



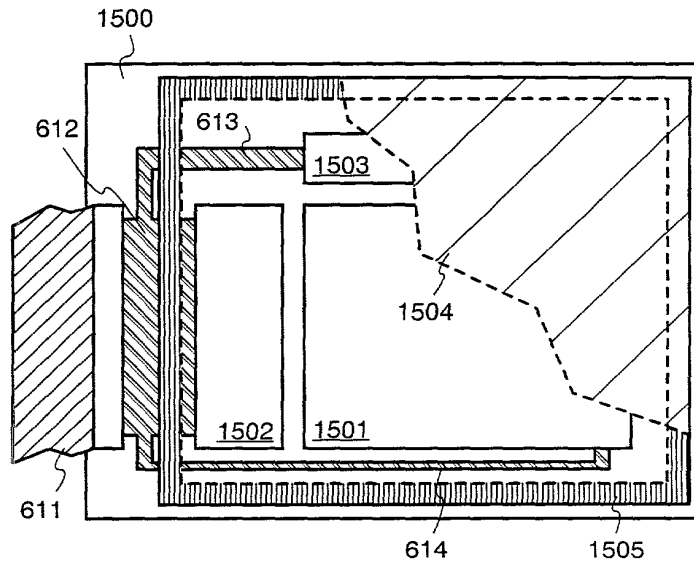


Fig. 15A

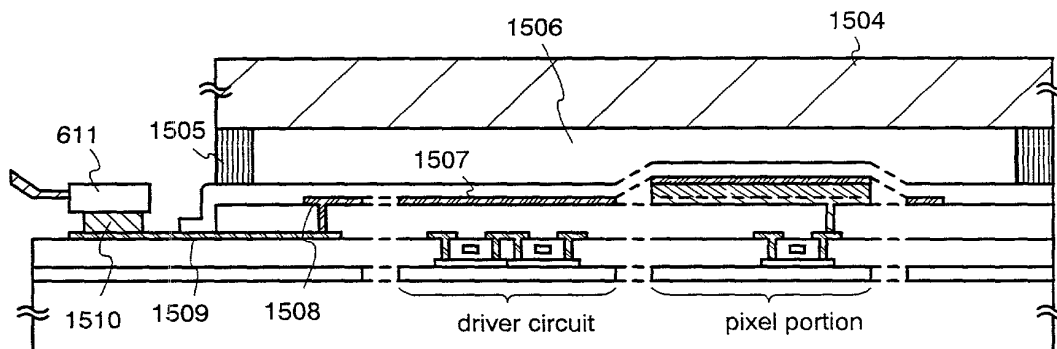


Fig. 15B

Fig. 16

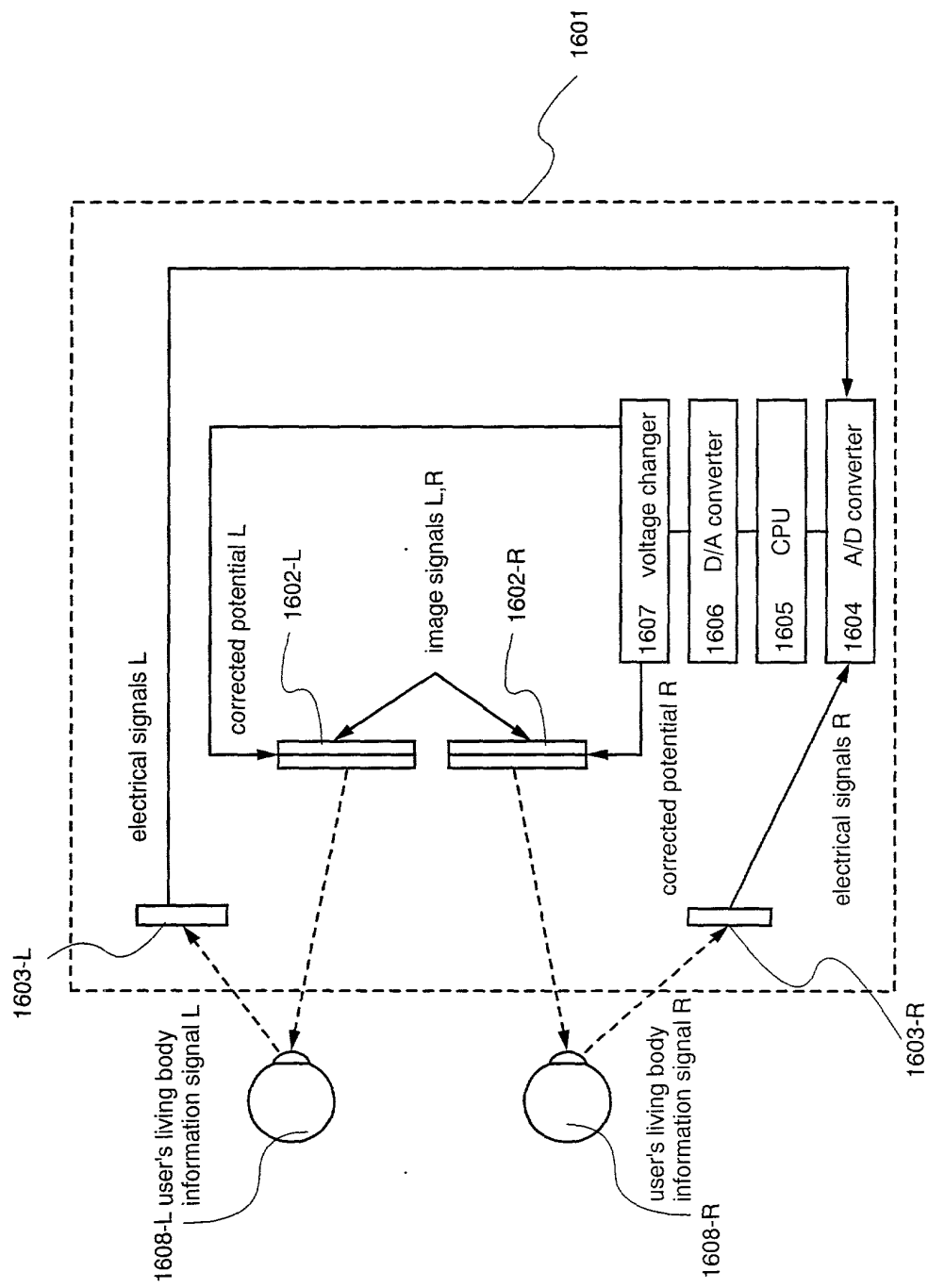




Fig. 17

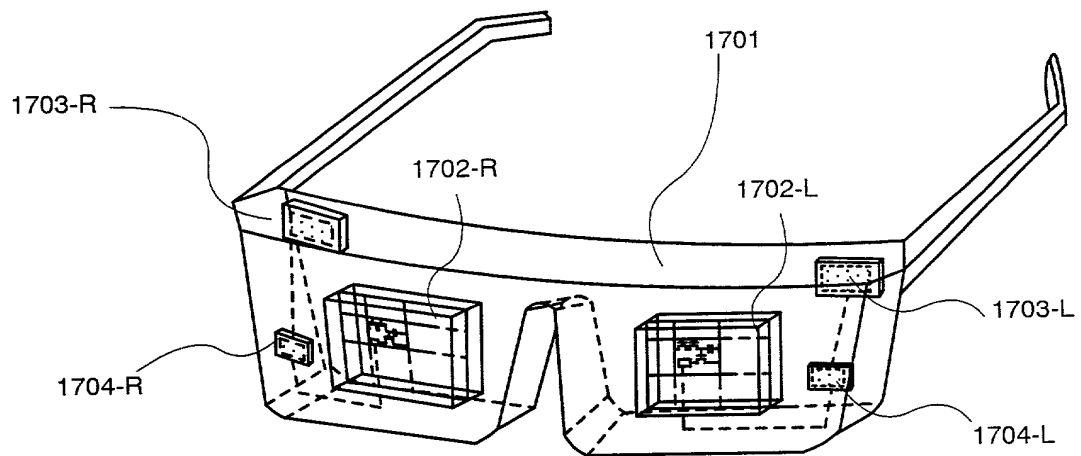


Fig. 18

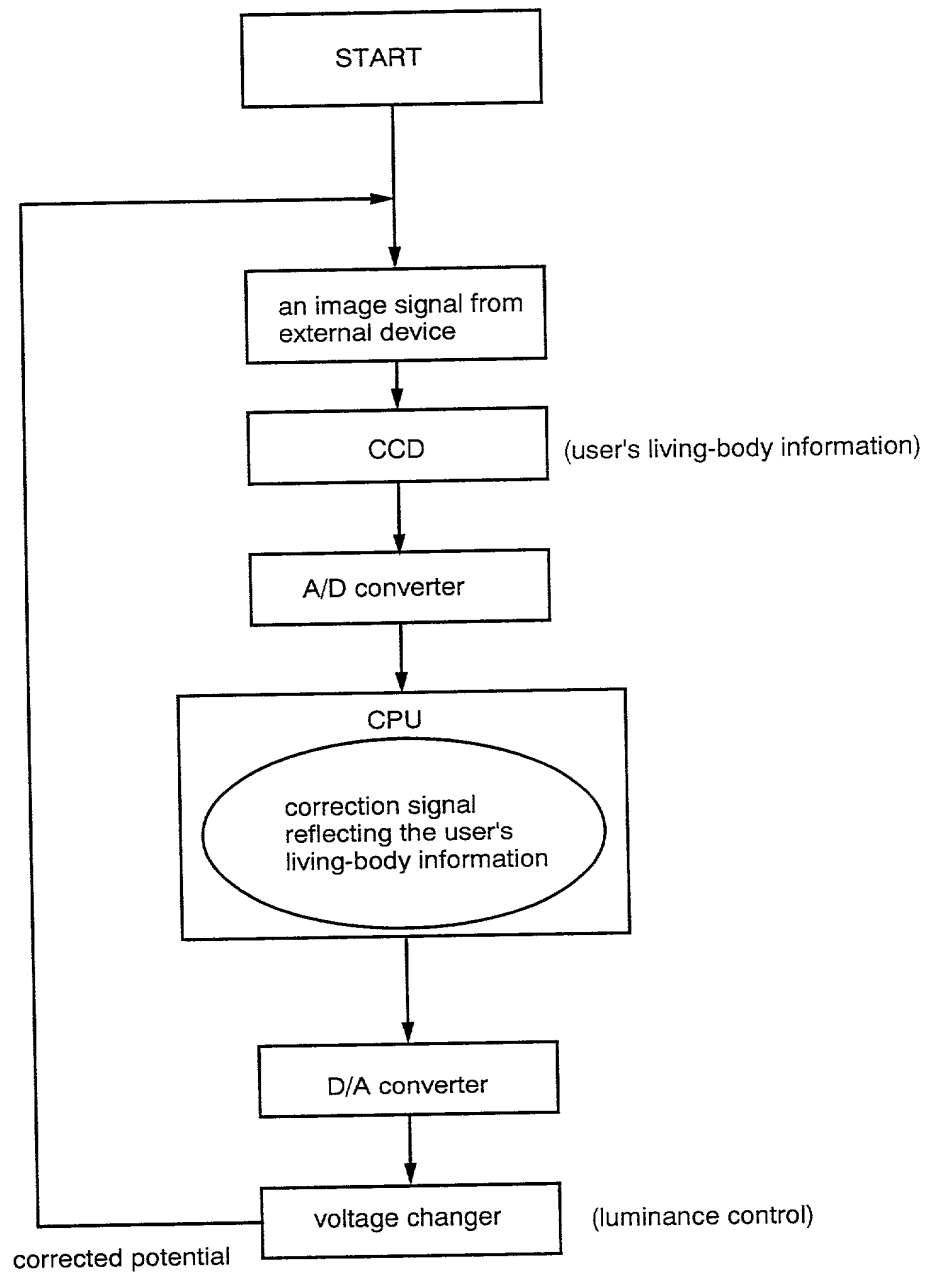


Fig. 19A

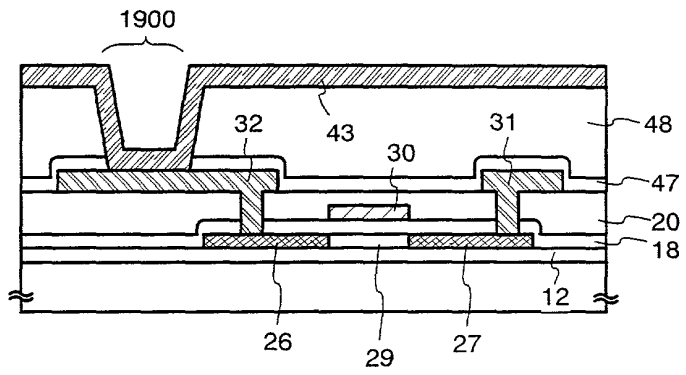


Fig. 19B

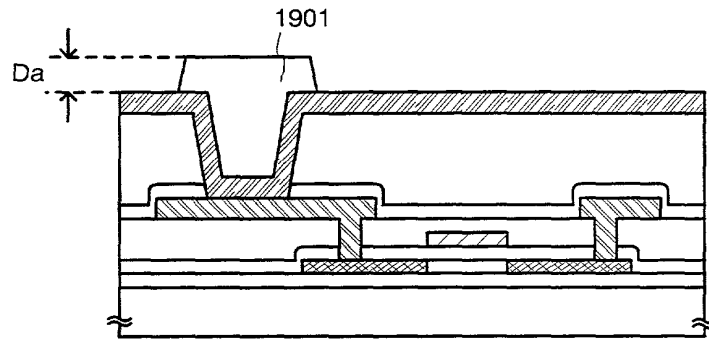
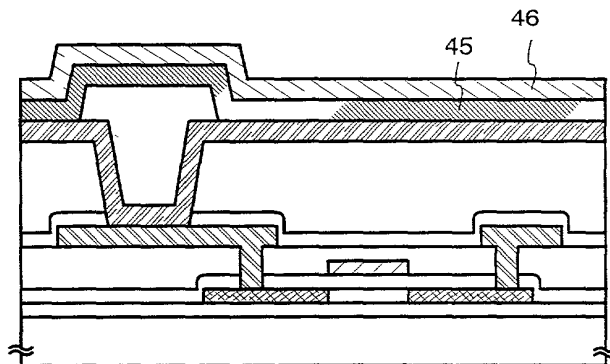


Fig. 19C



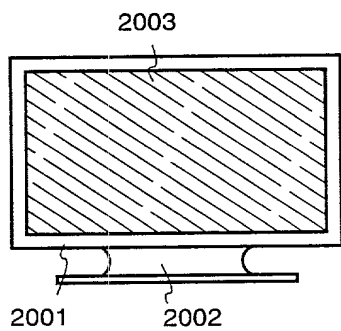


Fig. 20A

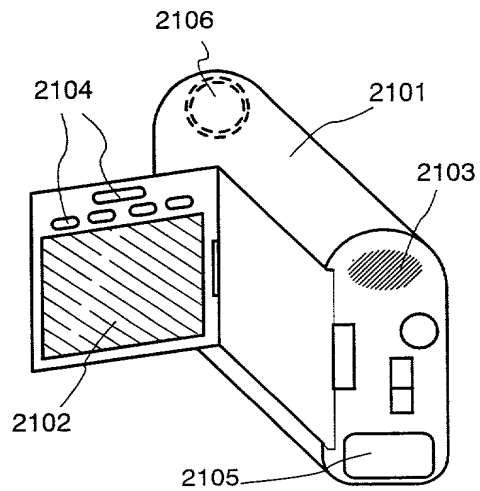


Fig. 20B

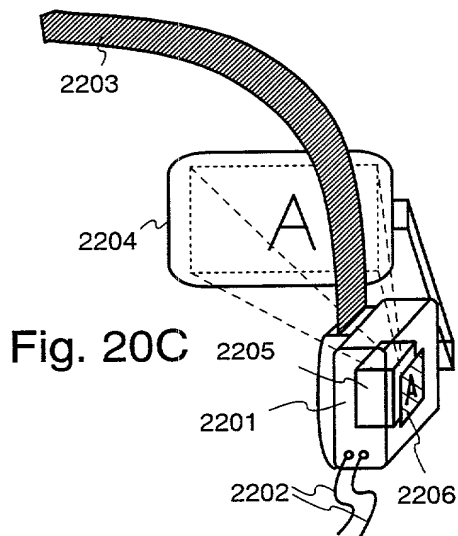


Fig. 20C

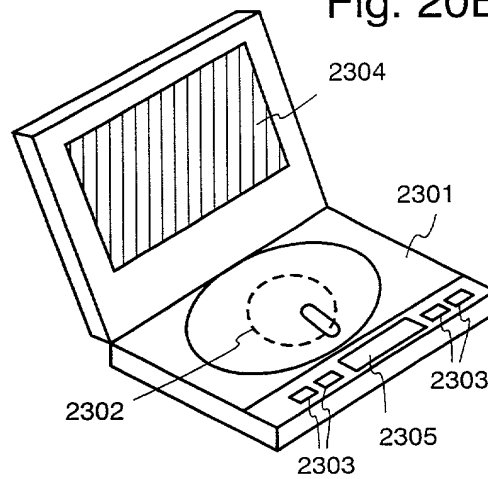


Fig. 20D

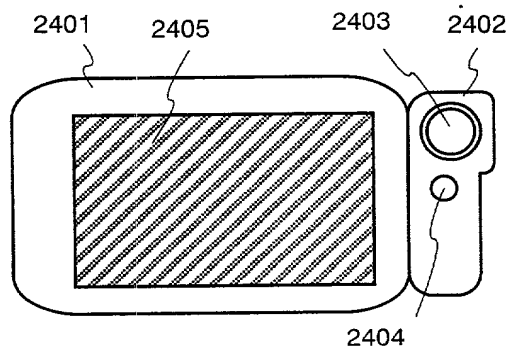


Fig. 20E

Fig. 21A

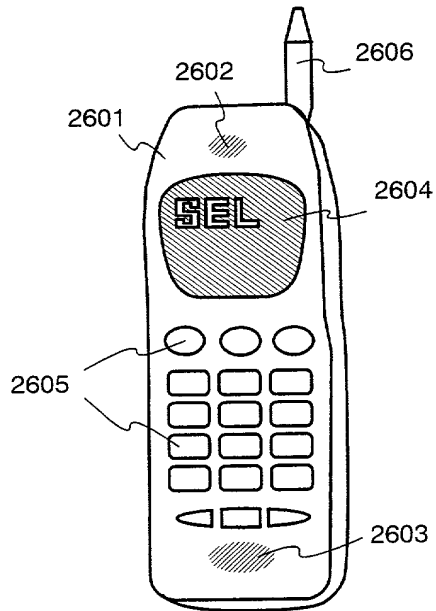


Fig. 21B

